

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Makoto MATSUOKA *et al.*
Serial No. : §53b Continuation of 09/534,521 filed 3/24/2000
Filed : 23 June 2003
For : METHOD AND APPARATUS FOR
SEPARATING SEMICONDUCTOR CHIPS
Group : Unassigned (Parent - 2825)
Examiner : Unassigned (Parent - Calvin Lee)
Conf. No. : Unassigned (Parent - 1401)

INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application

Commissioner of Patents

POB 1450

Alexandria, VA 22313-1450

23 June 2003

Sir:

Applicant respectfully submits the information listed on the attached Form PTO-1449 for entry and consideration in the present application, and to appear on the face of any patent issuing hereon.

This application is a continuation of Serial No. 09/534,521 filed 24 March 2000, now allowed, which is a continuation-in-part of Serial No. 09/516,504 filed 1 March 2000, now US 6,297,075 B1.

The information listed on the attached Form PTO-1449 was previously cited by or submitted to the Office in the prior applications. In accordance with MPEP §609A(2), no copies of the listed references are required. Further, in accordance with MPEP §609A(3), no concise explanation of relevance is required for the

information that is not in the English language because the relevance of the information does not differ from its relevance as explained in the prior applications.

This Information Disclosure Statement is being filed within three (3) months of the filing date of the above-identified application, and is therefore timely, so no Petition or fee is required for this submission.

Respectfully submitted,



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| Form PTO-1449 | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | ATTY. DKT. NO. 500.38284CP2 | SERIAL NO. Unassigned |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) | | APPLICANT Makoto MATSUOKA <i>et al.</i> | |
| | | FILING DATE 06/23/2003 | GROUP Unassigned |

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| Examiner Initial | Document Number | Date | Name | Class | Subclass | Filing Date |
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| Examiner | | Date Considered |